

EAST Search History

| Ref # | Hits | Search Query | DBs | Default Operator | Plurals | Time Stamp |
|-------|------|---|---|------------------|---------|---------------------|
| L1 | 4 | (circuit) and (source adj technology) and (target adj technology) and (resiz\$3) and (dc adj simulation) | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2009/05/13 12:58 |
| L2 | 1 | (analog adj circuit) and (source adj technology) and (target adj technology) and (resiz\$3) and (circuit adj blocks) | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2009/05/13 13:19 |
| L3 | 0 | (analog adj circuit) and (source adj technology) and (target adj technology) and (resiz\$3) and (function\$2 adj blocks) | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2009/05/13 13:20 |
| L4 | 1 | (circuit) and (source adj technology) and (target adj technology) and (resiz\$3) and ((node adj voltage\$2) with resistor) | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2009/05/13 13:32 |
| L5 | 1 | (circuit) and (source adj technology) and (target adj technology) and (resiz\$3) and (transistor\$2 or resistor \$2 or capacitor\$2) and ((server same client) same computer) | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2009/05/13 13:33 |
| L6 | 3 | (circuit) and (source adj technology) and (target adj technology) and ((resiz\$3) with (transistor\$2)) | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2009/05/13 13:42 |
| L7 | 6 | (circuit) and (source adj technology) and (target adj technology) and ((resiz\$3) same (transistor\$2)) | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2009/05/13 13:44 |

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| L8 | 1 | (circuit) and (source adj technology) and (target adj technology) and ((resiz\$3) same (transistor\$2) same ((output adj conductance) with (parasitic adj capacitance))) | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2009/05/13 13:46 |
| L10 | 1 | (analog adj circuit) and (source adj technology) and (target adj technology) and (resiz\$3 or scal\$3) and (parameter adj database) | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2009/05/13 15:16 |
| L11 | 1 | (source adj technology) and (target adj technology) and (resiz\$3 or scal\$3) and (parameter adj database) | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2009/05/13 15:17 |
| L12 | 1 | (source adj technology) and (target adj technology) and (migration adj engine) and (parameter adj database) | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2009/05/13 15:18 |
| L13 | 5 | ("2005028113").PN. | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2009/05/13 15:25 |
| L14 | 5 | ("2005028103").PN. | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2009/05/13 15:29 |
| L15 | 1 | (source adj technology) and (target adj technology) and (migration adj engine) and (computer same server same (client or user or customer)) | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2009/05/13 15:38 |

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| L16 | 1 | (source adj technology) and (target adj technology) and (computer same server same (client or user or customer)) | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2009/05/13 15:39 |
| L17 | 10 | (source adj technology) and (target adj technology) and (migration) and (network or internet) | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2009/05/13 15:45 |
| L18 | 1 | (source adj technology) and (target adj technology) and (migration) and (internet) | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2009/05/13 15:45 |
| L19 | 1 | (source adj technology) and (target adj technology) and (migration) and (internet) | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2009/05/13 15:45 |
| L20 | 1 | (source) and (target) and (technology with (migration adj engine)) and (computer same server same (client or user or customer)) | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2009/05/13 15:50 |
| S1 | 3943 | 716/1 | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2009/05/12 15:00 |
| S2 | 3138 | 716/2 | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2009/05/12 15:01 |
| S3 | 1370 | 716/3 | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2009/05/12 15:01 |

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| S4 | 0 | (716/1).ccls. and (analog adj circuit) and (source adj technology) and (target technology) | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2009/05/12 15:02 |
| S5 | 1 | (716/2).ccls. and (analog adj circuit) and (source adj technology) and (target technology) | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2009/05/12 15:02 |
| S6 | 4 | (716/3).ccls. and (analog adj circuit) and (source adj technology) and (target technology) | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2009/05/12 15:04 |
| S7 | 5 | ("716"/\$).ccls. and (analog adj circuit) and (source adj technology) and (target technology) | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2009/05/12 15:05 |
| S8 | 11 | (analog adj circuit) and (source adj technology) and (target technology) | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2009/05/12 15:05 |
| S9 | 10 | (analog adj circuit) and (source adj technology) and (target technology) and (resiz\$3 or scal\$3) | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2009/05/12 15:06 |
| S10 | 7 | (analog adj circuit) and (source adj technology) and (target adj technology) and (resiz\$3 or scal\$3) | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2009/05/12 15:07 |
| S11 | 6 | (analog adj circuit) and (source adj technology) and (target adj technology) and (resiz\$3) | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2009/05/12 15:08 |

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| S12 | 6 | (analog adj circuit) and (source adj technology) and (target adj technology) and (resiz\$3) and (transistor\$2 or resistor \$2 or capacitor\$2) | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2009/05/12 16:03 |
| S13 | 4 | (analog adj circuit) and (source adj technology) and (target adj technology) and (resiz\$3) and (transistor\$2 or resistor \$2 or capacitor\$2) and (Internet or network) | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2009/05/12 18:35 |
| S14 | 1 | (analog adj circuit) and (source adj technology) and (target adj technology) and (resiz\$3) and (transistor\$2 or resistor \$2 or capacitor\$2) and (server same user) | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2009/05/12 18:39 |
| S15 | 1 | (circuit) and (source adj technology) and (target adj technology) and (resiz\$3) and (transistor\$2 or resistor \$2 or capacitor\$2) and (server same user) | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2009/05/12 18:40 |
| S16 | 1 | (circuit) and (source adj technology) and (target adj technology) and (resiz\$3) and (server same user) | US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2009/05/12 18:40 |

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